

LQFP64	Pin Name	Type	Main Function	Alternate Function	Remap
PinNr.			(after Reset)		
45	PA12	I/O	PA12	USART1_RTS/ OTG_FS_DP/ CAN1_TX/ TIM1_ETR	-
46	PA13	I/O	JTMS-SWDIO	-	PA13
49	PA14	I/O	JTCK-SWCLK	-	PA14
50	PA15	I/O	JTDI	SPI3_NSS/I2S3_WS	TIM2_CH1_ETR/ PA15/ SPI1_NSS
26	PB0	I/O	PB0	ADC12_IN8/ TIM3_CH3/ ETH_MII_RXD2	TIM1_CH2N
27	PB1	I/O	PB1	ADC12_IN9/ TIM3_CH4/ ETH_MII_RXD3	TIM1_CH3N
28	PB2	I/O	PB2/ BOOT1	-	-
55	PB3	I/O	JTD0	SPI3_SCK/I2S3_CK	PB3/ TRACESW0/ TIM2_CH2/ SPI1_SCK
56	PB4	I/O	NJTRST	SPI3_MISO	PB4/ TIM3_CH1/ SPI1_MISO
57	PB5	I/O	PB5	I2C1_SMBA/ SPI3_MOSI/I2S3_SD/ ETH_MII_PPS_OUT/ ETH_RMII_PPS_OUT	TIM3_CH2/ SPI1_MOSI/ CAN2_RX
58	PB6	I/O	PB6	I2C1_SCL/ TIM4_CH1	USART1_TX/ CAN2_TX
59	PB7	I/O	PB7	I2C1_SDA/ TIM4_CH2	USART1_RX
61	PB8	I/O	PB8	TIM4_CH3/ ETH_MII_TXD3	I2C1_SCL/ CAN1_RX
62	PB9	I/O	PB9	TIM4_CH4	I2C1_SDA/ CAN1_TX
29	PB10	I/O	PB10	I2C2_SCL/ USART3_TX/ ETH_MII_RX_ER	TIM2_CH3
30	PB11	I/O	PB11	I2C2_SDA/ USART3_RX/ ETH_MII_TX_EN/ ETH_RMII_TX_EN	TIM2_CH4