

| LQFP64 PinNr. | Pin Name | Type | Main Function | Alternate Function | Remap |
|------------------|---------------------|------|---------------|--|-------------------------------------|
| | | | (after Reset) | | |
| 33 | PB12 | I/O | PB12 | SPI2_NSS/I2S2_WS/I2C2_SMBA/ USART_CK/ TIM1_BKIN/ CAN2_RX/ ETH_MII_TXD0/ETH_RMII_TXD0 | - |
| 34 | PB13 | I/O | PB13 | SPI2_SCK/I2S2_CK/ USART3_CTS/ TIM_CH1N/ CAN2_TX/ ETH_MII_TXD1/ETH_RMII_TXD1 | - |
| 35 | PB14 | I/O | PB14 | SPI2_MISO/ TIM1_CH2N/ USART3_RTS | - |
| 36 | PB15 | I/O | PB15 | SPI2_MOSI/I2S2_SD/ TIM1_CH3 | - |
| 8 | PC0 | I/O | PC0 | ADC12_IN10 | - |
| 9 | PC1 | I/O | PC1 | ADC12_IN11/ ETH_MII_MDC/ETH_RMII_MDC | - |
| 10 | PC2 | I/O | PC2 | ADC12_IN12/ ETH_MII_TXD2 | - |
| 11 | PC3 | I/O | PC3 | ADC12_IN13/ ETH_MII_TX_CLK | - |
| 24 | PC4 | I/O | PC4 | ADC12_IN14/ ETH_MII_RXD0/ETH_RMII_RXD0 | - |
| 25 | PC5 | I/O | PC5 | ADC12_IN15/ ETH_MII_RXD1/ETH_RMII_RXD1 | - |
| 37 | PC6 | I/O | PC6 | I2S2_MCK | TIM3_CH1 |
| 38 | PC7 | I/O | PC7 | I2S3_MCK | TIM3_CH2 |
| 39 | PC8 | I/O | PC8 | - | TIM3_CH3 |
| 40 | PC9 | I/O | PC9 | - | TIM3_CH4 |
| 51 | PC10 | I/O | PC10 | UART4_TX | USART3_TX/ SPI3_SCK/ I2S3_CK |
| 52 | PC11 | I/O | PC11 | UART4_RX | USART3_RX/ SPI3_MISO |
| 53 | PC12 | I/O | PC12 | USART5_TX | USART3_CK/ SPI3_MOSI/ I2S3_SD |
| 2 | PC13- Tamper-RTC | I/O | PC13 | TAMPER-RTC | - |
| 3 | PC14- OSC_IN | I/O | PC14 | OSC32_IN | - |
| 4 | PC15- OSC_OUT | I/O | PC15 | OSC32_OUT | - |
| 54 | PD2 | I/O | PD2 | TIM3_ETR/ USART5_RX | - |